

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((memor\$3 or storag\$3 or stor\$3 or buffer) and (computer or processor or CPU or PC or microprocessor or controller) and level near3 confidence and characteriz\$3 and (outout\$4 or display\$3 or monitor\$3) and process\$3 and performance).CLM.	US-PGPU B	OR	ON	2006/03/30 15:11

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	60500	((382/128,129,130,131,132,133,134) or (600/407) or (422/50,51,52,53,54,73) or (435/4,6,7.2,7.23,40.5,40.51,41,70.1,70.4,163,440,441,449) or (436/86,94,501) or (702/19,20,21,22)).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2006/03/30 15:15
L2	26815	1 and (computer or processor or microprocessor or controller or CPU)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:21
L3	25054	2 and (stor\$4 or memor\$4 or buffer or database)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:21
L4	24029	3 and (monitor\$3 or display\$3 or output\$3 or screen\$3)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:17

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L6	744	4 and (signal\$3 near4 characteriz\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:28
L7	744	6 and (calculat\$4 or comput\$4 or measur\$3 or evaluat\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:27
L8	29	7 and level near4 confidence	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:28
L9	1419	((calculat\$4 or comput\$4 or measur\$3 or evaluat\$4) near4 (level near4 confidence))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:30

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L10	25	9 and (signal\$3 near4 characteriz\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:21
L11	22	10 and (computer or processor or microprocessor or controller or CPU)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:21
L12	18	11 and (stor\$4 or memor\$4 or buffer or database)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:21
L13	18	12 and (signal\$3 near4 characteriz\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:25

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L14	4898	4 and (array43 or microarray\$3)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:26
L15	13890	4 and (array\$3 or microarray\$3)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:26
L16	0	("15and(gridorsubgridorsub-grid)").PN.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2006/03/30 15:27
L17	2557	15 and(grid or subgrid or sub-grid)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:29

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L18	2417	17 and compar\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:27
L19	2414	18 and (calculat\$4 or comput\$4 or measur\$3 or evaluat\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:27
L20	133	19 and level near4 confidence	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:28
L21	5	20 and (signal\$3 near4 characteriz\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:30

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L22	159	9 and(grid or subgrid or sub-grid)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:30
L23	4	22 and (signal\$3 near4 characteriz\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:29
L24	5047	((calculat\$4 or comput\$4 or measur\$3 or evaluat\$4) same (level near4 confidence))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:30
L25	116	24 and (signal\$3 near4 characteriz\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:30

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L26	19	25 and(grid or subgrid or sub-grid)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/03/30 15:31
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IEEE JNL IEE Journal or Magazine

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IEEE STD IEEE Standard

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 Kurosawa, I.; Nakagawa, H.; Aoyagi, M.; Kosaka, S.; Takada, S.;
[Solid-State Circuits IEEE Journal of](#)
 Volume 26, Issue 4, April 1991 Page(s):572 - 577
 Digital Object Identifier 10.1109/4.75057
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2. Toward robust integrated circuits: The embryonics approach
 Mange, D.; Sipper, M.; Stauffer, A.; Tempesti, G.;
[Proceedings of the IEEE](#)
 Volume 88, Issue 4, April 2000 Page(s):516 - 543
 Digital Object Identifier 10.1109/5.842998
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3. Energy discrimination and counting electronics for Computer-Aided Radioactive Particle Tr
 Wintenberg, A.L.; Clonts, L.G.;
[Nuclear Science Symposium Conference Record, 2003 IEEE](#)
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